PERIPHERAL MANAGEMENT SYSTEM

ABSTRACT OF THE DISCLOSURE

A peripheral management system includes a system chip, a peripheral I/O control device and a control management device. The control management device is electrically connected between the system chip and the peripheral I/O device in series. In a downstream transmission mode, the control management device asserts a first processing signal to the peripheral I/O control device in response to a control signal from the system chip in a first state, and asserts a second processing signal to the peripheral I/O control device in response to an external control signal in a second state. In an upstream transmission mode, the control management device asserts a third processing signal to the system chip in response to another external control signal. Each of the external control signals are asserted by a remote terminal.